# NALLA NARASIMHA REDDY
**Education Society's Group of Institutions – Integrated Campus**
Approved by AICTE, New Delhi, Affiliated to JNTU – Hyderabad
## SCHOOL OF ENGINEERING

## DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

**Name of the Subject : Compiler Design**                          **Subject Code : CS601PC**

**Year/Sem        : III-II**                                        **Regulation    : R16**

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|---|---|
| **CO1** | Able to **define** different types of translators used in programming |
| **CO2** | **Explain** symbol table organization and role of semantic analysis in compiler design |
| **CO3** | Able to **construct** a top down and bottom up parser |
| **CO4** | **List** various code generation techniques |
| **CO5** | Able to **design** a Lexical analyzer |

**Mapping Matrix of CO's and PO's:**

| | PO1 | PO2 | PO3 | PO4 | PO5 | PO6 | PO7 | PO8 | PO9 | P10 | P11 | P12 | PSO1 | PSO2 |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **CO1** | X | X | X | | | | | | | | | | | |
| **CO2** | X | | X | | | | | | | | | | | |
| **CO3** | X | X | X | | X | | | | | | | | X | X |
| **CO4** | X | | | | | | | | | | | | X | |
| **CO5** | X | X | X | | X | | | | | | | | X | X |

Course                          Program                                    HOD
Coordinator                     Coordinator